US-PAT-NO:

6324233

DOCUMENT-IDENTIFIER: US 6324233 B1

TITLE:

Reception of modulated carriers having

asymmetrical

sidebands

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Detailed Description Text - DETX (4):

FIG. 1 also illustrates the following features in broken lines. The synchronous demodulator SDEM and the filter FILT may form part of a receiver REC. The receiver REC may include input circuitry INP for deriving the modulated carrier MC from a reception signal RS. The

synchronous demodulator

SDEM and the filter FILT may wholly or partially form part
of an integrated

circuit IC.

Detailed Description Text - DETX (12):

FIG. 4 illustrates an example of a receiver in accordance with the invention

which includes the features shown, and described with reference to FIG. 1. In the FIG. 4 receiver, the input circuitry INP comprises a tuner TUN and an intermediate-frequency filter IFF. The synchronous demodulator SDEM is formed by mixer circuits MIXI, MIXQ and a phase-locked loop PLL which comprises a phase detector PHD, a loop filter LPF and a voltage controlled oscillator VCO.

Detailed Description Text - DETX (32):

There are numerous ways of physically spreading functions or functional elements over various units. In this respect, the drawings are very diagrammatic, each representing only one possible embodiment of the invention. For example, with reference to FIG. 1, the input circuitry INP may be wholly or partially included in the integrated circuit IC. In another example, with reference to FIG. 4, the intermediate-frequency filter IFF may be incorporated in the tuner TUN.